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1 A 10-b 125-MHz CMOS digital-to-analog converter (DAC) with threshold voltage compensated current sources*Shu-Yuan Chin; Chung-Yu Wu;*Solid-State Circuits, IEEE Journal of , Volume: 29 , Issue: 11 , Nov. 1994
Pages:1374 - 1380
[\[Abstract\]](#) [\[PDF Full-Text \(536 KB\)\]](#) **IEEE JNL**
2 The design of 8-bit CMOS digital to analog converter*Tan, G.H.; Surparjo, B.S.; Wagiran, R.; Sidek, R.;*Semiconductor Electronics, 2001. Proceedings. ICSE 2001. 2000 IEEE International Conference on , 13-15 Nov. 2000
Pages:231 - 234
[\[Abstract\]](#) [\[PDF Full-Text \(200 KB\)\]](#) **IEEE CNF**
3 The design of high-speed pipelined analog-to-digital converters using voltage-mode sampling and current mode processing techniques*Yu-Yee Liow; Chung-Yu Wu;*Circuits and Systems, 2002. ISCAS 2002. IEEE International Symposium on , Volume: 3 , 26-29 May 2002
Pages:III-117 - III-120 vol.3
[\[Abstract\]](#) [\[PDF Full-Text \(472 KB\)\]](#) **IEEE CNF**
4 A 10-b, 100 MS/s, 2.8 mW CMOS switched-current DAC for low-power/low-voltage signal processing applications*Jin-Sheng Wang; Chin-Long Wey;*

Circuits and Systems, 1998. Proceedings. 1998 Midwest Symposium on , 9-12

1998
Pages:526 - 529

[\[Abstract\]](#) [\[PDF Full-Text \(40 KB\)\]](#) IEEE CNF

5 A two-dimensional analysis of output overshoot mechanism in DAC current switch

Hamasaki, T.; Wada, T.; Kobori, T.;

Bipolar Circuits and Technology Meeting, 1989., Proceedings of the 1989 , 18-Sept. 1989

Pages:148 - 151

[\[Abstract\]](#) [\[PDF Full-Text \(252 KB\)\]](#) IEEE CNF

6 A 16 bit+sign monotonic precise current DAC for sensor applications

Horsky, P.;

Design, Automation and Test in Europe Conference and Exhibition, 2004. Proceedings , Volume: 3 , 16-20 Feb. 2004

Pages:34 - 38 Vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(335 KB\)\]](#) IEEE CNF

7 A 1.5-V 14-bit 100-MS/s self-calibrated DAC

Yonghua Cong; Geiger, R.L.;

Solid-State Circuits, IEEE Journal of , Volume: 38 , Issue: 12 , Dec. 2003

Pages:2051 - 2060

[\[Abstract\]](#) [\[PDF Full-Text \(911 KB\)\]](#) IEEE JNL

8 A 14-bit 1.8-V 20-mW 1-mm² CMOS DAC

Tiilikainen, M.P.;

Solid-State Circuits, IEEE Journal of , Volume: 36 , Issue: 7 , July 2001

Pages:1144 - 1147

[\[Abstract\]](#) [\[PDF Full-Text \(112 KB\)\]](#) IEEE JNL

9 The effect of redundancy on measurement

Collins, O.M.; Vasudev, N.;

Information Theory, IEEE Transactions on , Volume: 47 , Issue: 7 , Nov. 2001

Pages:3090 - 3096

[\[Abstract\]](#) [\[PDF Full-Text \(411 KB\)\]](#) IEEE JNL

10 Electrical characterisation of domestic refrigeration compressors

Kattakayam, T.A.; Srinivasan, K.;

Science, Measurement and Technology, IEE Proceedings- , Volume: 144 , Iss. 3 , May 1997

Pages:123 - 126

[\[Abstract\]](#) [\[PDF Full-Text \(392 KB\)\]](#) IEE JNL

11 Power consumption optimization of 8 bit, 2 MHz voltage scaling subranging CMOS 0.5 μ m DAC

Maloberti, F.; Rivoir, R.; Torelli, G.;
Electronics, Circuits, and Systems, 1996. ICECS '96., Proceedings of the Third International Conference on , Volume: 2 , 13-16 Oct. 1996
Pages:1162 - 1165 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(412 KB\)\]](#) [IEEE CNF](#)

12 Digital alternative sinusoidal current waveform for virtual resistance measurements

Tarchanidis, K.N.; Koukourlis, C.;
Virtual and Intelligent Measurement Systems, 2001, IEEE International Works on. VIMS 2001 , 19-20 May 2001
Pages:52 - 56

[\[Abstract\]](#) [\[PDF Full-Text \(276 KB\)\]](#) [IEEE CNF](#)

13 An integrated CMOS potentiostat for miniaturized electroanalytical instrumentation

Reay, R.J.; Kounaves, S.P.; Kovacs, G.T.A.;
Solid-State Circuits Conference, 1994. Digest of Technical Papers. 41st ISSCC 1994 IEEE International , 16-18 Feb. 1994
Pages:162 - 163

[\[Abstract\]](#) [\[PDF Full-Text \(288 KB\)\]](#) [IEEE CNF](#)

14 Design of DAC-drivers for resistive actuators

Maloberti, F.; Tagliani, F.; Torelli, G.;
Circuits and Systems, 1990., IEEE International Symposium on , 1-3 May 1990
Pages:1713 - 1716 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(404 KB\)\]](#) [IEEE CNF](#)

15 Low power programmable front-end for a multichannel neural recording interface

Gosselin, B.; Simard, V.; Sawan, M.;
Electrical and Computer Engineering, 2003. IEEE CCECE 2003. Canadian Conference on , Volume: 2 , 4-7 May 2003
Pages:911 - 914 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(321 KB\)\]](#) [IEEE CNF](#)

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